

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	21	loop same (prolog\$2 near4 epilog\$4 near4 kernel)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2003/10/23 15:58
S2	0	modulo adj1 variable adj1 expansion	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2003/10/23 16:47
S3	0	modulo near4 variable near4 expansion	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2003/10/23 16:47
S4	19	modulo near4 expansion	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2003/10/27 17:09
S5	16	loop near4 (bit adj1 mask\$1)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2003/10/23 16:48
S6	0	modulo adj1 schedul\$3 near4 buffer\$1	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2003/10/27 17:11
S7	0	modulo adj1 schedul\$3 adj1 buffer\$1	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2003/10/27 17:10
S8	0	(modulo adj1 schedule) near4 buffer\$1	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2003/10/27 17:11
S9	17	modulo adj1 schedule	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2003/10/27 17:12
S10	321	loop adj1 buffer	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2003/10/30 10:10
S11	0	(zero?overhead) adj1 loop adj1 buffer	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2003/10/27 17:13

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S12	0	(loop adj1 buffer) same (modulo adj1 schedule)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2003/10/27 17:13
S13	0	(modulo adj1 schedule) and (loop adj1 buffer)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2003/10/27 17:13
S14	0	(modulo adj1 schedule) and (loop adj1 buffer)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2003/10/27 17:14
S15	0	(loop adj1 buffer) near4 schedule	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2003/10/27 17:14
S16	0	(loop adj1 stage) near4 (bit adj1 mask)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2003/10/30 10:08
S17	1	(loop adj1 stage) near4 buffer	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2003/10/30 10:04
S18	6	(loop adj1 stage) same buffer	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2003/10/30 10:06
S19	0	(loop adj1 stage) same (bit adj1 mask\$1)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2003/10/30 10:05
S20	0	(loop adj1 stage\$1) same (bit adj1 mask\$1)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2003/10/30 10:05
S21	8	(loop adj1 stage\$1) same buffer\$1	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2003/10/30 10:06
S22	817	(pipeline adj1 stage\$1) same buffer\$1	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2003/10/30 10:06
S23	138	(pipeline adj1 stage\$1) near4 buffer\$1	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2003/10/30 10:06

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S24	19	(pipeline adj1 stage\$1) near4 (stor\$3 near4 buffer\$1)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2003/10/30 10:07
S25	0	(loop adj1 stage) near4 (mask\$1)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2003/10/30 10:08
S26	16	(loop adj1 buffer) near4 stage	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2003/10/30 10:11
S27	8	("5511178" "5526397" "5579493" "5872989" "5951679" "6085315" "6269440" "6367071").PN.	USPAT	OR	OFF	2003/10/30 10:12
S28	1	zero adj1 overhead adj1 loop adj1 buffer	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2003/10/30 12:50
S29	0	zero adj1 overhead adj1 buffer	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2003/10/30 12:50
S30	1	zero adj1 overhead adj1 loop near4 buffer	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2003/10/30 12:50
S31	1	(zero adj1 overhead adj1 loop) near4 buffer	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2003/10/30 12:50
S32	1	(zero adj1 overhead near4 loop) near4 buffer	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2003/10/30 12:50
S33	1	(loop adj1 buffer) near4 (unroll\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2003/10/30 13:09
S34	1	(loop adj1 buffer) same (unroll\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2003/10/30 13:17
S35	2052	loop near4 interrupt\$1	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2003/10/30 16:11

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S36	0	(loop adj1 iteration) near4 interrupt\$1	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2003/10/30 16:12
S37	1	(loop adj1 end) near4 interrupt\$1	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2003/10/30 16:12
S38	3	(loop adj1 (iteration or cycle)) near4 interrupt\$1	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2003/10/30 16:14
S39	0	(loop adj1 (iteration or cycle) near4 end) near4 interrupt\$1	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2003/10/30 16:14
S40	0	(loop adj1 (iteration or cycle) near4 end) same interrupt\$1	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2003/10/30 16:19
S41	13	(loop adj1 iteration) same interrupt\$1	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2003/11/03 09:41
S42	41	(loop adj1 (iteration or cycle) near4 end)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2003/10/30 16:19
S43	10	((("6085315") or ("5867711") or ("6418489") or ("5835776") or ("5809308") or ("5664193") or ("5579493") or ("6598155") or ("6269440") or ("6367071"))).PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2003/11/03 08:37
S44	1	("6421744").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2003/11/03 09:41
S45	456	(712/244).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2004/10/18 10:02
S46	225	(712/241).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2004/10/18 10:02

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S47	77	(717/150).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2004/10/18 10:02
S48	133	(717/160).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2004/10/18 10:02
S49	83	(717/161).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2004/10/18 10:53
S50	0	(functional adj1 unit) near4 (instruction adj1 cache) near4 (individual or separate or sing\$4)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/10/18 11:04
S51	22	(functional adj1 unit) near4 (instruction adj1 cache)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/10/18 10:58
S52	8	(functional adj1 unit\$1) near4 cache\$1 near4 (individual or separate or unique or personal or own)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/10/18 11:04
S53	0	(functional adj1 unit\$1) near4 (instruction adj1 cache\$1) near4 (individual or separate or unique or own)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/10/18 11:05
S54	0	(functional adj1 unit\$1) with ((instruction adj1 cache\$1) near4 (individual or separate or unique or own))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/10/18 11:06
S55	15	(functional adj1 unit\$1) same ((instruction adj1 cache\$1) near4 (individual or separate or unique or own))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/10/18 11:06
S56	495	(712/244).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2005/05/26 15:21
S57	249	(712/241).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2005/05/26 15:21

EAST Search History

S58	88	(717/150).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2005/05/26 15:21
S59	141	(717/160).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2005/05/26 15:21
S60	94	(717/161).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2005/05/26 15:21
S61	2	dispatch\$3 near4 buffer\$3 near4 (loop\$1 near4 kernel)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/05/26 15:24
S62	2	dispatch\$3 with (buffer\$3 near4 (loop\$1 near4 kernel))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/05/26 15:23
S63	5	dispatch\$3 with (buffer\$3 with (loop\$1 near4 kernel))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/05/26 15:23
S64	6	dispatch\$3 with (buffer\$3 with (loop\$1 with kernel))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/05/26 16:55
S65	4	dispatch\$3 near4 buffer\$3 near4 (loop\$1 near4 instruction\$1)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/05/26 15:24
S66	10	dispatch\$3 near4 (buffer\$3 or cach\$3 or stor\$3) near4 (loop\$1 near4 instruction\$1)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/05/26 16:49
S67	14	dispatch\$3 with ((buffer\$3 or cach\$3 or stor\$3) near4 (loop\$1 near4 instruction\$1))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/05/26 16:33
S68	19	dispatch\$3 with ((buffer\$3 or cach\$3 or stor\$3) with (loop\$1 near4 instruction\$1))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/05/26 16:56

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S69	16	dispatch\$3 near4 ((buffer\$3 or cach\$3 or stor\$3) near4 loop\$1)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/05/26 16:40
S70	21	dispatch\$3 with ((buffer\$3 or cach\$3 or stor\$3) near4 loop\$1)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/05/26 16:45
S71	24	(dispatch\$3 near4 (buffer\$3 or cach\$3 or stor\$3)) with loop\$1	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/05/26 16:42
S72	63	dispatch\$3 with ((buffer\$3 or cach\$3 or stor\$3) with loop\$1)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/05/26 16:45
S73	2	(buffer\$3 or cach\$3 or stor\$3) near4 (loop\$1 near4 instruction\$1) near4 kernel	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/05/26 16:52
S74	3	(buffer\$3 or cach\$3 or stor\$3) with ((loop\$1 near4 instruction\$1) near4 kernel)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/05/26 16:50
S75	6	(buffer\$3 or cach\$3 or stor\$3) with ((loop\$1 near4 instruction\$1) with kernel)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/05/26 16:50
S76	6	(buffer\$3 or cach\$3 or stor\$3) near4 loop\$1 near4 kernel	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/05/26 16:52
S77	62	(buffer\$3 or cach\$3 or stor\$3) with (loop\$1 near4 kernel)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/05/26 16:52
S78	7	dispatch\$3 same (buffer\$3 with (loop\$1 with kernel))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/05/26 16:55
S79	7	dispatch\$3 same (buffer\$3 same (loop\$1 with kernel))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/05/26 16:56
S80	9	dispatch\$3 same (buffer\$3 same (loop\$1 same kernel))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/05/26 16:56

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S81	68	dispatch\$3 same ((buffer\$3 or cach\$3 or stor\$3) with (loop\$1 near4 instruction\$1))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2005/05/26 16:56
S82	524	(712/244).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2006/01/10 10:33
S83	268	(712/241).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2006/01/10 10:33
S84	98	(717/150).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2006/01/10 10:33
S85	154	(717/160).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2006/01/10 10:33
S86	107	(717/161).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2006/01/10 10:33
S87	1	(loop\$3 near4 buffer\$1) with (schedul\$3 near4 information\$1)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/01/10 11:29
S88	2	(loop\$3 near4 buffer\$1) same (schedul\$3 near4 information\$1)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/01/10 11:43
S89	0	(loop\$3 near4 buffer\$1) with (schedul\$3 near4 tag\$1)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/01/10 11:43
S90	0	(loop\$3 with buffer\$1) with (schedul\$3 near4 tag\$1)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/01/10 11:43
S91	2	(loop\$3 with buffer\$1) with (schedul\$3 with tag\$1)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/01/10 13:12

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S92	2	(loop\$3 with buffer\$1) same (schedul\$3 with tag\$1)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/01/10 13:12
S93	7	(loop\$3 with schedul\$3 with tag\$1)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/01/10 13:46
S94	270	(712/241).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2006/01/20 11:25
S95	525	(712/244).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2006/01/20 11:42
S96	99	(717/150).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2006/01/20 11:42
S97	155	(717/160).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2006/01/20 11:42
S98	108	(717/161).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2006/01/20 11:42
S99	7	(loop\$3 with schedul\$3 with tag\$1)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/01/20 11:47
S10 0	149	(loop\$3 near4 buffer\$3) near4 (schedul\$3 or iteration\$1 or count\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/01/20 13:20
S10 1	26	(loop\$3 adj1 buffer\$3) near4 (schedul\$3 or iteration\$1 or count\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/01/20 14:37
S10 2	1	("6988190").PN.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/01/20 14:37

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S10 3	6	("5381533" "5579493" "6145076" "6304962" "6347383" "6598155").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/01/20 14:37
S10 4	8	(US-6671799-\$ or US-6598155-\$ or US-6347383-\$ or US-6304962-\$ or US-6145076-\$ or US-5579493-\$ or US-5381533-\$ or US-6988190-\$). did.	USPAT	OR	OFF	2006/01/20 14:53
S10 5	1	(functional adj1 units) near4 (loop near4 execut\$3 near4 unit)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/01/23 08:39
S10 6	3	(functional adj1 units) near4 (loop near4 unit)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/01/23 08:41
S10 7	333	(loop near4 execut\$3 near4 unit)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/01/23 08:39
S10 8	0	(loop near4 execut\$3 near4 unit) near4 (superscalar)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/01/23 08:40
S10 9	722	(loop near4 unit) near4 ((execut\$3 or process\$3 or function\$3) adj1 unit\$1)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/01/23 08:40
S11 0	1	(loop adj1 unit) near4 ((execut\$3 or process\$3 or function\$3) adj1 unit\$1)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/01/23 08:41
S11 1	0	(functional adj1 units) with (loop adj1 unit)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/01/23 08:41
S11 2	10	(functional adj1 units) with (loop near4 unit)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/01/23 08:42
S11 3	79	((functional or process\$3 or execut\$3) adj1 units) with (loop near4 unit)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/01/23 08:43
S11 4	12	loop adj1 execut\$3 adj1 unit	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/01/23 08:43

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S11 5	287	(712/241).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2007/06/24 20:36
S11 6	546	(712/244).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2007/06/24 20:36
S11 7	108	(717/150).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2007/06/24 20:37
S11 8	167	(717/160).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2007/06/24 20:37
S11 9	122	(717/161).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2007/06/24 20:37
S12 0	45	loop near4 buffer\$1 near4 fetch\$3	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/07/10 08:51
S12 1	1	("4,626,988").PN.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/07/10 08:51
S12 2	304	(712/241).CCLS.	US-PGPUB; USPAT	OR	OFF	2007/06/24 20:36
S12 3	592	(712/244).CCLS.	US-PGPUB; USPAT	OR	OFF	2007/06/24 20:37
S12 4	124	(717/150).CCLS.	US-PGPUB; USPAT	OR	OFF	2007/06/24 20:37
S12 5	187	(717/160).CCLS.	US-PGPUB; USPAT	OR	OFF	2007/06/24 20:37
S12 6	145	(717/161).CCLS.	US-PGPUB; USPAT	OR	OFF	2007/06/24 20:37
S12 7	53	loop near4 buffer\$1 near4 fetch\$3	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/06/24 20:47

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S12 8	312	(712/241).CCLS.	US-PGPUB; USPAT	OR	OFF	2007/08/23 09:58
S12 9	595	(712/244).CCLS.	US-PGPUB; USPAT	OR	OFF	2007/08/23 09:58
S13 0	125	(717/150).CCLS.	US-PGPUB; USPAT	OR	OFF	2007/08/23 09:59
S13 1	197	(717/160).CCLS.	US-PGPUB; USPAT	OR	OFF	2007/08/23 09:59
S13 2	147	(717/161).CCLS.	US-PGPUB; USPAT	OR	OFF	2007/08/23 09:59
S13 3	53	loop near4 buffer\$1 near4 fetch\$3	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/08/23 10:00
S13 4	18	((functional adj1 unit\$1) same ((instruction adj1 cache\$1) near4 (individual or separate or unique or own))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/08/23 10:00
S13 5	9	(loop adj1 stage\$1) same buffer\$1	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/08/23 10:00
S13 6	25	(dispatch\$3 near4 (buffer\$3 or cach\$3 or stor\$3)) with loop\$1	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/08/23 10:01
S13 7	97	((functional or process\$3 or execut\$3) adj1 units) with (loop near4 unit)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/08/23 10:01
S13 8	16	loop adj1 execut\$3 adj1 unit	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/08/23 10:01
S13 9	10	((((functional or process\$3 or execut\$3) adj1 units) with (loop near4 unit)).clm.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/08/23 10:01
S14 0	6	(loop adj1 execut\$3 adj1 unit).clm.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/08/23 10:01
S14 1	29	(loop\$3 adj1 buffer\$3) near4 (schedul\$3 or iteration\$1 or count\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/08/23 10:02

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S14 2	12	((loop\$3 adj1 buffer\$3) near4 (schedul\$3 or iteration\$1 or count\$3)).clm.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/08/23 10:02
S14 3	1	((Hwu-Wen\$) and (Merten-Mat\$)). in.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/08/23 10:04
S14 4	1	((Hwu-W\$) and (Merten-M\$)).in.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/08/23 10:04
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DB Powell - US Patent 5,606,698, 1997 - Google Patents

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AE Eichenberger, ES Davidson - Proceedings of the 28th annual international symposium on ..., 1995 - portal.acm.org

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R Lauwereins, M Engels, M Ade, JA Peperstraete - Computer, 1995 - ieeexplore.ieee.org

... it is implicitly embedded in an infi- nite time **loop**. ... 1;O FIR-R #cO;#c+1 **Schedule**: WHILE TRUE ... execution time, amount of program, data and **buffer** memory and ...

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H Oh, N Dutt, S Ha - Proceedings of the 2005 international conference on ..., 2005 - portal.acm.org

... For a pair of nodes, we can change the **loop** count value for a source node or a sink node to represent a **buffer** optimal **schedule**. ...

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A framework for resource-constrained rate-optimal softwarepipelining - all 13 versions »

R Govindarajan, ER Altman, GR Gao - Parallel and Distributed Systems, IEEE Transactions on, 1996 - ieeexplore.ieee.org

... test cases, less than 4%, the ILP **schedule** was worse in terms of either initiation rate or **buffer** requirement ... adopt as our motivating example the **loop** £ in Fig ...

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Enhancing **loop** buffering of media and telecommunications applications using low-overhead predication - all 6 versions »

JW Sias, HC Hunter, W Hwu - Proceedings of the 34th annual ACM/IEEE international ..., 2001 - doi.ieeecomputersociety.org

... this results in increased perfor- mance and increased **loop buffer** efficiency. ... NOPs in even the optimal mod- ulo **schedule** of the inner **loop**, predicated **loop** ...

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... IN COMPILE-TIME CODE **SCHEDULE** 38, pp. ... xhe Memory Conflict **Buffer** (MCB) scheme of the ... ware supports such code reordering by (1) detecting the **loop** iterations. ...

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PK Murthy, SS Bhattacharyya - System Synthesis, 1999. Proceedings. 12th International ..., 1999 - ieeexplore.ieee.org

... product of all the **loop** factors on the path from the leaf node ... In figure 4, for the **schedule** tree on the left, we have for , and ... 5.3 **Buffer merging formulae** ...

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[schedule reuse - all 9 versions »](#)

R Ponnusamy, J Saltz, A Choudhary - 1993 - ACM Press New York, NY, USA
 ... **schedule** to prefetch required off-processor data. ... from inspectors (eg communication
 schedules, **loop** iteration ... data copies with on-processor **buffer** locations ...
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 ... Given an RSFG, how does one find a **schedule** which minimizes **buffer** space requirement while still exe- cuting the program graph at an optimal computation rate? ...
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 ... contain the **kernel** of the modulo sched- uled **loop**. ... to-one correspondence between
 schedule location and **buffer** en- try ... 1). In order to produce a **prologue** and an ...
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GR Uh - LECTURE NOTES IN COMPUTER SCIENCE, 2003 - Springer
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T Chiueh, FH Hsu - 21st International Conference on Distributed Computing - doi.ieeecomputersociety.org
 ... Figure 3: Function **Prologue** Code ... (2) A **loop** statement that copies user input into
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[Architectural support for execution control of prologue and epilogue periods of loops in a VLIW ... - all 2 versions »](#)

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 ... on a **loop** counter (LC) register, which epilogu edistinguishing register and is responsive
 to the state kept track of the number of **prologue** and **kernel** iterations ...
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ED Granston, EJ Stotzer, SD Krueger, TD Anderson - 2003 - freepatentsonline.com
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 register unit ... full, ie, all the software pipeline **loop** instructions from ...
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 the **kernel** of the ... In this state, one of the **loop buffer** units is in use with ...
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W Caarls, PP Jonker, H Corporaal - Proc. 20th IEEE International Parallel and Distributed ..., 2006 - ph.tn.tudelft.nl
 ... Program 7 The main **loop** of the ... instance[1].**kernel**; }; pixelbypixel(args, arguments,
prologue, **kernel**); } ... to deal with the appropriate **buffer** delays, and of ...
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 register unit ... The software pipeline **loop** procedure is preceded by an SPLOOP ...
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T Anderson, MD Asal, EJ Stotzer - 2003 - freepatentsonline.com
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buffer register are ... are provided for the pipelined software **loop** procedure in ...
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... and method for executing a nested **loop** program with a software pipeline **loop** procedure in a digital ...

EJ Stotzer, MD Asal - 2003 - freepatentsonline.com

... The SPLOOP **prologue** is now complete. ... memory/cache unit 31 can be eliminated by storing the outer **loop** instruction sequence in a **buffer** storage unit ...

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